



Dkt. 2271/65888

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Yasutoshi HIRANO

Serial No.: 09/955,885

Group Art Unit: 2186

Date Filed: September 19, 2001

Examiner: D. Tran

For: DIGITAL SIGNAL PROCESSOR AND MODEM USING THE SAME

**Mail Stop RCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**PETITION FOR EXTENSION OF TIME**

Applicant hereby requests an extension of time of one month for filing a Request For Continued Examination Under 37 C.F.R. §1.114 in response to the April 19, 2004 final Office Action in connection with the above-identified application.

Enclosed is a check to cover the \$110.00 statutory extension fee for filing a response within the first month pursuant to §1.136(a) by other than a small entity.

The Commissioner is authorized to charge any additional fees, or credit any overpayment, to our Deposit Account No. 03-3125.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
	August 19, 2004
Paul Teng Reg. No. 40,837	Date